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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/676,822	09/30/2003	William H. Whitted	GOOGP007	3063
23689	7590	04/19/2005	EXAMINER	
Jung-hua Kuo Attorney At Law PO Box 3275 Los Altos, CA 94024			LEVI, DAMEON E	
			ART UNIT	PAPER NUMBER
			2841	

DATE MAILED: 04/19/2005


Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/676,822

Applicant(s)

WHITTED, WILLIAM H. 

Examiner

Dameon E. Levi

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 30 September 2003.
- 2a) ☐ This action is FINAL. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-22 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-9 and 13-20 is/are rejected.
- 7) ☐ Claim(s) 10-12, 21 and 22 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 30 September 2003 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____.
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____.

DETAILED ACTION

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1,3,5,-7,and 13-15 are rejected under 35 U.S.C. 102(b) as being anticipated by Lee US Patent 5801920.

Regarding claim 1, Lee discloses an assembly comprising:

a first mounting structure including two generally parallel and opposing spacers(for example, see elements 51, Figs 1-4);

a plurality of supports (for example, see elements 52, Figs 1-4)extending from the spacers, the supports being configured to engage holes defined on a surface of a first electronics component to be mounted on the first mounting structure(for example, see elements 4, Figs 1-4),

a base portion extending between the two generally parallel and opposing spacers(for example, see elements 5, Figs 1-4), the spacers and the supports being generally orthogonal to the base portion(for example, see elements 51,52,5 Figs 1-4).

Regarding claim 3, Lee discloses further comprising a fastening mechanism(for example, see elements 6, Figs 1-4) to secure the first mounting structure to the first electronics component.

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Regarding claim 5, Lee discloses wherein the supports are tapered so that the supports

decrease in size as the supports extend away from the spacers(for example, see elements 52, Figs 1-4).

Regarding claim 6, Lee discloses wherein the supports are made of a material that is harder than the material of the holes defined on the surface of the first electronics component (for example, see elements 52, Figs 1-4).

Regarding claim 7, Lee discloses an electronics base (for example, see elements 52, Figs 1-4), the first mounting structure being rigidly attached to the electronics base, the first mounting structure and the electronics base being separately formed.

Regarding claim 13, Lee discloses an apparatus comprising:

component mounting means(for example, see elements 5, Figs 1-4);

for securing a first electronics component to an electronics base, including means for spacing(for example, see elements 51, Figs 1-4) the first electronics component from the electronics base, and

means for engaging a plurality of holes defined on a surface of the first electronics component(for example, see elements 52,4, Figs 1-4) to be mounted on the mounting mechanism, said means for engaging extends from said means for spacing; (for example, see elements 51,52 Figs 1-4);

and means for securing the component mounting means to the first electronics component(for example, see elements 6,53,31 Figs 1-4).

Regarding claim 14, Lee discloses wherein said means for spacing defines a means

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for allowing a fastening mechanism to engage thereto(for example, see elements 53 Figs 1-4);.

Regarding claim 15, Lee discloses wherein said means for engaging includes means for engaging holes defined on the surface of the first electronics component to be mounted on said component mounting means(for example, see elements 52,4 Figs 1-4).

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claim 2 is rejected under 35 U.S.C. 103(a) as being unpatentable over Lee US Patent 5801920 in view of Kling US Patent 4342068.

Regarding claim 2, Lee discloses the instant claimed invention except wherein each spacer defines an orifice configured to allow a fastening mechanism to pass therethrough and engage thereto.

Kling discloses an apparatus wherein each spacer defines an orifice(for example, see element 66,44 Figs 1-3) configured to allow a fastening mechanism to pass therethrough and engage thereto.

Accordingly, it would have been obvious to one of ordinary skill in the art at the time the invention was made to have included an orifice in the spacer as taught by Kling in the

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apparatus as taught by Lee for the purpose of receiving a bolt in order secure the apparatus(see Kling, Abstract)

Claim 4 is rejected under 35 U.S.C. 103(a) as being unpatentable over Lee US Patent 5801920 in view of Parson US Patent 6473309

Regarding claim 4, Lee discloses the instant claimed invention except wherein the fastening mechanism comprises a hook and loop material.

Parson discloses an apparatus wherein a fastening mechanism comprises a hook and loop material(for example, see elements 110, 160, Figs 3, 5).

Accordingly, it would have been obvious to one of ordinary skill in the art at the time the invention was made to have used a hook and loop fastener as taught by Parson in the apparatus as taught by Lee for the purpose of providing fastening attachments without requiring the use of tools(see Parson column 2, lines 60-65)

Claim 8 is rejected under 35 U.S.C. 103(a) as being unpatentable over Lee US Patent 5801920 .

Regarding claim 8, Lee discloses the instant claimed invention except further comprising an electronics base, the first mounting structure being integrally formed from the electronics base. However, it would have been obvious to one of ordinary skill in the art at the time the invention was made to have integrally formed the mounting structure from the electronics base, since it has been held that forming in one piece an

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article which has been formerly been formed in two pieces and put together involves only routine skill in the art. Howard v. Detroit Stove works, 10 U.S. 164(1893).

Claim 9 is rejected under 35 U.S.C. 103(a) as being unpatentable over Lee US Patent 5801920 in view of Butler US Patent 2783416

Regarding claim 9, Lee discloses the instant claimed invention except a plurality of sharp points configured to face and be secured to a surface of a second electronics component onto which the first mounting structure and the first electronics component are stacked.

Butler discloses an assembly comprising a plurality of sharp points configured to face and be secured to a surface of a second electronics component onto which the first mounting structure and the first electronics component are stacked(for example, see elements 14,16, Figs 1-8).

Accordingly, it would have been obvious to one of ordinary skill in the art at the time the invention was made to have included the plurality of sharp points as taught by Butler in the assembly as taught by Lee for the purpose of permitting electrical units to be stacked together (see Butler column 1, lines 45-50)

Regarding claims 17-20, the methods disclosed therein are deemed as being inherent in the assembly of the claimed apparatus since the elements recited therein as taught and suggested in the prior art of record.

Allowable Subject Matter

Claims 10-12 and 21-22 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

The following is a statement of reasons for the indication of allowable subject matter:

The prior art does not teach or suggest a second mounting structure including:

two generally parallel and opposing second spacers; a plurality of second supports extending from the second spacers, the second supports being configured to engage holes defined on a surface of the second electronics component to be mounted on the second mounting structure, a second base portion extending between the two generally parallel and opposing second spacers, the second spacers and the second supports being generally orthogonal to the second base portion.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dameon E. Levi whose telephone number is (571) 272-2105. The examiner can normally be reached on Mon.-Fri. (9:00 - 5:00).

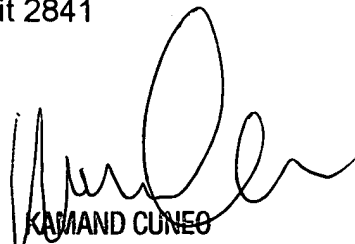
If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kamand Cuneo can be reached on (571) 272-1957. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Dameon E Levi
Examiner
Art Unit 2841

DEL



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